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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/976,310	10/12/2001	Bidyut Parruck	AZA-003-3D/2001-P006	3100
293	7590	08/21/2006	EXAMINER	
Ralph A. Dowell of DOWELL & DOWELL P.C. 2111 Eisenhower Ave Suite 406 Alexandria, VA 22314			DUONG, DUC T	
			ART UNIT	PAPER NUMBER
			2616	

DATE MAILED: 08/21/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

DETAILED ACTION

Response to Arguments

1. Applicant's arguments with respect to claims 45, 49-52 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

3. Claims 45, 46, and 49-51 are rejected under 35 U.S.C. 102(e) as being anticipated by Opalka et al (US Patent 6,259,699 B1).

Regarding to claims 45 and 49, Opalka discloses a switching device (fig. 4), comprising a switch fabric, and a multi-service segmentation and reassembly MS-SAR integrated circuit (fig. 14) having a first bus interface (left PHY interface) and a second bus interface (right PHY interface), the second bus interface being coupled to the switch

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fabric 4 (noted the forwarding engine shown in fig. 14 is the same forwarding engine shown in fig. 4), network information passing through the first bus interface of the MS-SAR, the network information including a first flow of cell-protocol traffic and a second flow of packet-protocol traffic (col. 13 lines 9-25), the MS-SAR being in either a first way operation with cell-based switch fabric as the switch fabric or a second way for operation with a packet-based switch fabric as the switch fabric (col. 13 lines 23-25 and col. 14 lines 40-47).

Regarding to claim 46, Opalka discloses the switching device comprises a plurality of integrated circuits (forwarding engine), each of the plurality integrated circuits being structurally identical to the MS-SAR integrated circuit, each of the integrated circuits being coupled to the switch fabric (fig. 4 col. 10 lines 11-19).

Regarding to claim 50, Opalka discloses the MS-SAR operates in an ingress mode, the MS-SAR receiving both cell-protocol traffic and packet-protocol traffic (fig. 14 col. 13 lines 63-67).

Regarding to claim 51, Opalka discloses the MS-SAR operates in an egress mode, the MS-SAR outputting both cell-protocol traffic and packet-protocol traffic (fig. 14 col. 14 lines 22-37).

Allowable Subject Matter

4. Claim 47, 48, and 52 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
5. Claims 5 and 11 are allowed.

Conclusion

6. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.


7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Duc T. Duong whose telephone number is 571-272-3122. The examiner can normally be reached on M-F (9:00 AM-6:00 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Huy D. Vu can be reached on 571-272-3155. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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HUY D. VU
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2011